

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kevin Torek et al.

Title: SEMICONDUCTOR FABRICATION THAT INCLUDES SURFACE TENSION CONTROL

Docket No.: 303.871US1

Serial No.: 10/789,800

Filed: February 27, 2004

Due Date: June 19, 2006

Examiner: Tu-Tu V. Ho

Group Art Unit: 2818

MS AF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

X Response Under 37 CFR 1.116 (20 pgs.).

X Communication Concerning Related Applications (1 pg.).

If not provided for in a separate paper filed herewith, Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.


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By: 

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 19 day of June, 2006.


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